

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,046,434 B1
APPLICATION NO. : 10/020527
DATED : May 16, 2006
INVENTOR(S) : DiJaili et al.

Page 1 of 4

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

Item 56, Page 2, Other Publications, Column 1, Line 42, change "Jong" to --Jeong--

Drawings

Sheet 2, change "Figure 2" to the figure depicted herein below, wherein the reference "201" has been removed.

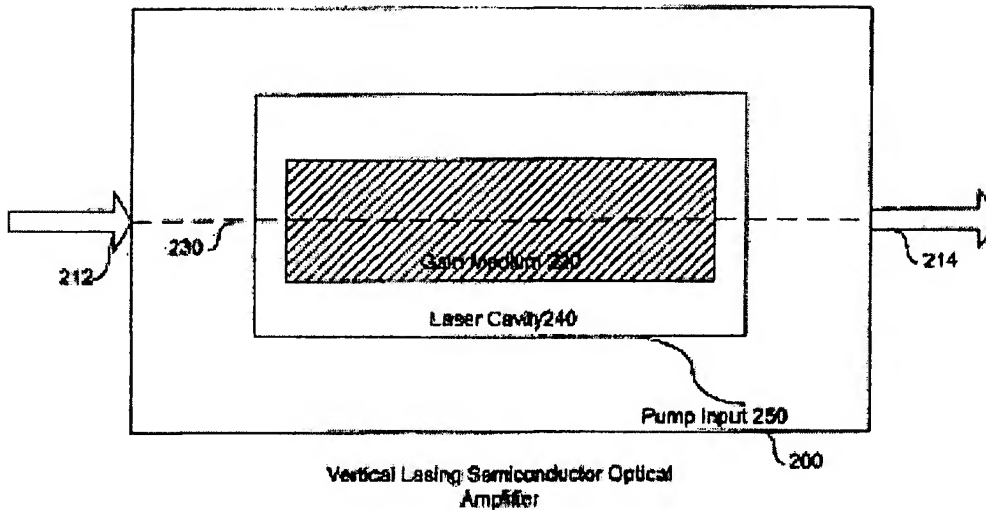


Figure 2

Signed and Sealed this

Thirtieth Day of November, 2010

David J. Kappos

David J. Kappos
Director of the United States Patent and Trademark Office

Drawings

Sheet 6, change "Figure 4C" to the figure depicted herein below, wherein the Top Cladding Layer has been labeled --407--

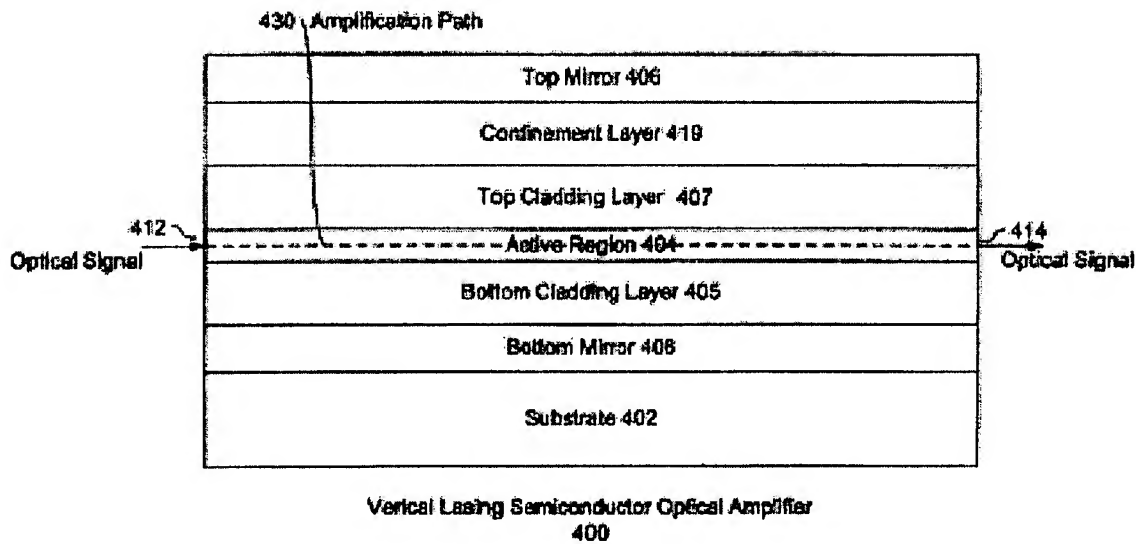


Figure 4C

Drawings

Sheet 11, change "Figure 7A" to the figure depicted herein below, wherein the NAND gate has been labeled --700--

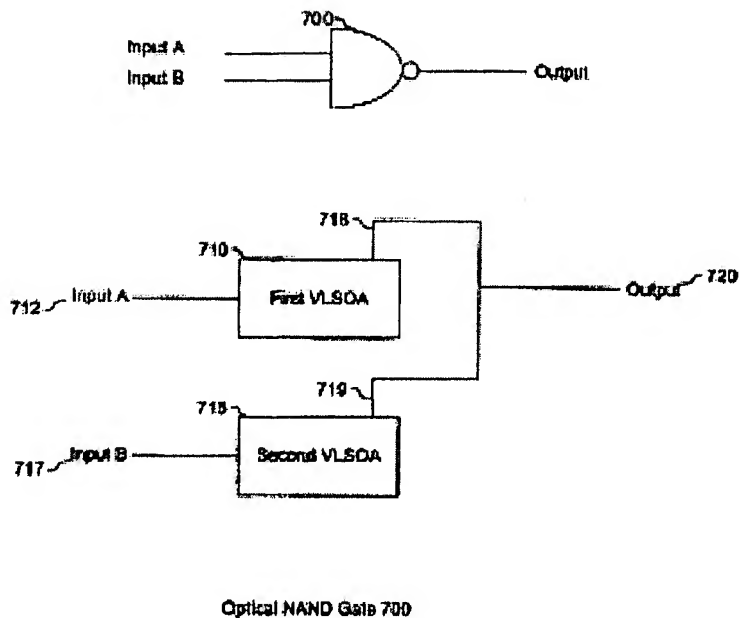
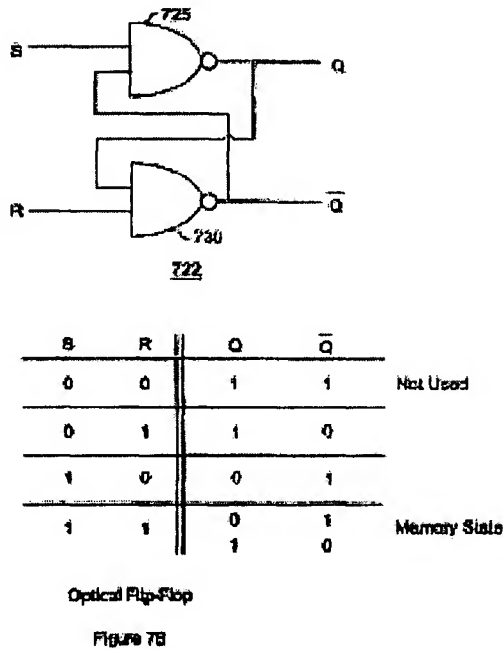


Figure 7A

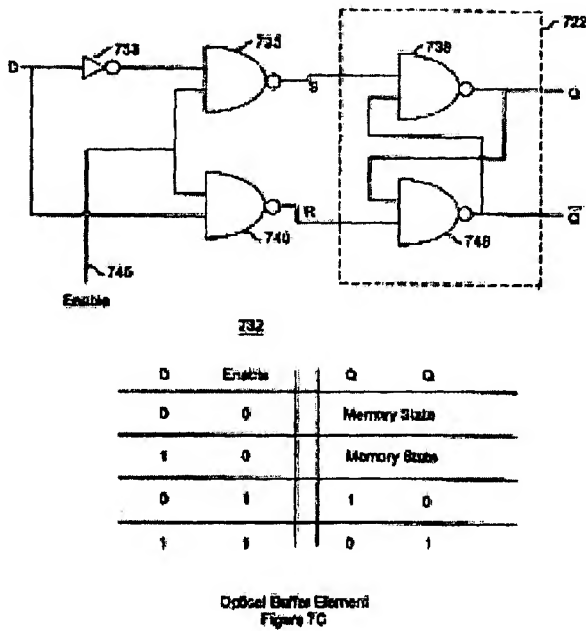
Drawings

Sheet 12, change “Figure 7B” to the figure depicted herein below, wherein the 2nd and 3rd row of the outputs have been change to --1-- and --0--, and --0-- and --1--, respectively.



Drawings

Sheet 13, change “Figure 7C” to the figure depicted herein below, wherein the 3rd and 4th row of the outputs have been change to --1-- and --0--, and --0-- and --1--, respectively.



Column 4

Line 53, change “115” to --110--

Column 11

Line 5, change “421” to --412--

Line 24, change “410” to --310--

Line 25, change “220” to --320--

Line 40-41, change “10 dB” to --20 dB--

Line 43, change “30 dB” to --60 dB--

Column 12

Line 17, change “510” to --110--

Column 15

Line 18, change “FIG. 17B” to --FIG. 7B--